

**Notice of References Cited**Application/Control No.  
09/915,396Applicant(s)/Patent Under  
Reexamination  
YAMAGUCHI ET AL.Examiner  
DuyVu n DeoArt Unit  
1765

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	U	Wolf et al., Silicon Processing for the VLSI Era, vol. 1, 1986, pages 181-182, 191-195, 407-408.
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\*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)  
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.